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[10191/822]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : MUELLER, W. et al.
Serial No. : 09/169,060
Filed : October 9, 1998
For : DEVICE AND METHOD FOR CONTROLLING A GENERATOR

Examiner : P. Medley
Group Art Unit : 2834

Honorable Commissioner
for Patents
Washington, D.C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Honorable Commissioner for Patents, Washington, DC 20231.

Dated: October 30, 2001 Reg. No. 36,197

Signature:

Jong H. Lee

RESPONSE

SIR:

This response addresses the final Office Action dated July 12, 2001. Reconsideration is respectfully requested in view of the following remarks.

REMARKS

Claims 1-6, 8-18, 20-24, and 27-37 are pending in the above-referenced application. Applicants thank the Examiner for indicating that claims 11, 12, 23 and 24 are allowed.

The Examiner has objected to the drawings under 37 C.F.R. § 1.83(a), and the Examiner has asserted that the drawings fail to show the free wheeling diode and short-circuiting transistor used together, as well as the transistors coupled in series. Contrary to the Examiner's assertions, Figure 2 clearly depicts the freewheeling diode and short-circuiting transistor used together as described in the specification at page 6, lines 12-24. The